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**LIQUID CRYSTAL DISPLAY MODULE**  
**MODEL: AWT-320240T35N02**  
**Customer's No.:**

|            |
|------------|
| Acceptance |
|            |

10 -1 Floor, No.192, Tahtung Road,  
 Sec. 3, Hsi-Chih City,  
 Taipei Hsien, Taiwan

|                         |
|-------------------------|
| Approved and Checked by |
|                         |

| Approved by   | Checked by   | Made by  |
|---|--|--|
| <br>2012/03/05<br>NICK | <br>2012/03/05<br>JOE | <br>2012/03/05<br>TOM |

|                              |               |                         |                 |                     |               |
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## 1. General Description

AWT-320240T35N02 is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit and a backlight unit. The panel size is 3.5 inch and the resolution is 320x240, the panel can display up to 262K colors. The LCM can be easily accessed by micro-controller via parallel interface.

## 2. Physical Features

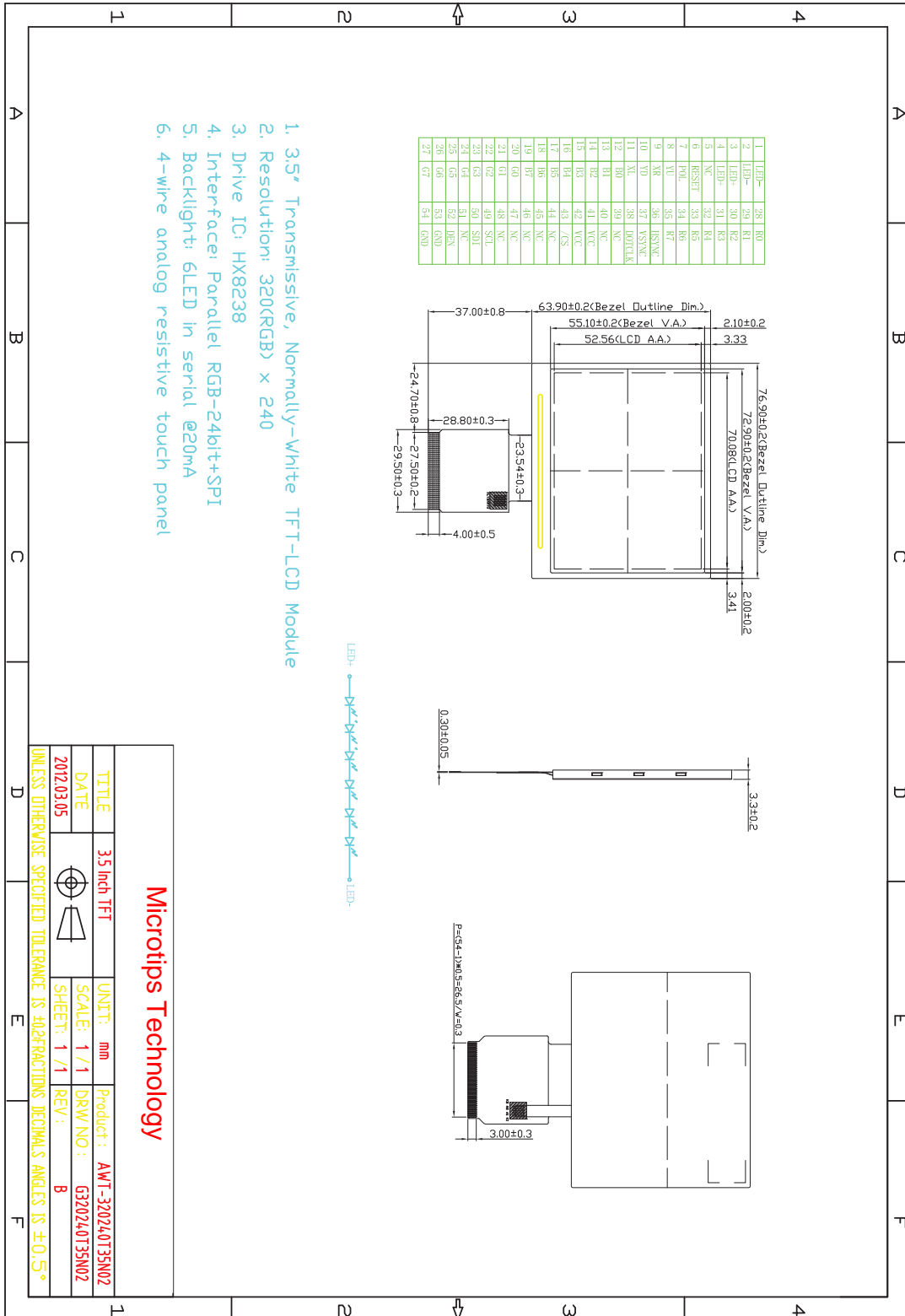
|                   |  |
|-------------------|--|
| Display Mode      | TFT-LCD Module                           |
|                   | Active matrix TFT, Transmissive type     |
| Display Format    | Graphic 320×RGB×240 Dot-matrix           |
| Input Data        | 24 bit RGB with Serial Interface\CCIR656 |
| Viewing Direction | 6 O'clock                                |
| Drive             | HX8238-A                                 |

## 3. Mechanical Specification

| Item                | Contents          | Unit |
|---------------------|-------------------|------|
| Module size (W×H×T) | 63.9 × 76.9 × 3.3 | mm   |
| Number of dots      | 320(RGB) × 240    | ---  |
| Active area (W×H)   | 70.08 × 52.56     | mm   |

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# 4. Outline



|                       |        |                  |          |              |        |
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## 5. Absolute Maximum Ratings

| Item                  | Symbol | Min  | Max     | Unit | Remark          |
|-----------------------|--------|------|---------|------|-----------------|
| Power Voltage         | VCC    | -0.3 | 3.6     | V    | Note1、<br>Note2 |
| Input Voltage         | VIN    | -0.3 | VCC+0.3 | V    |                 |
| Operating temperature | TOPR   | -20  | 70      | °C   |                 |
| Storage temperature   | TSTR   | -30  | 80      | °C   |                 |
| Humidity              | ---    | ---  | 90      | %RH  | ---             |

Remark:

Note 1) The HX8238 may be permanently damaged if it is used under the condition exceeding the above absolute maximum values. It is also recommended to use the HX8238 within the limit of its electric characteristics during normal operation. Exceeding the conditions may lead to malfunction of HX8238 and affect its credibility.

Note 2) The voltage from VSS.

## 6. Electrical Characteristics

| Item                    |         | Symbol | Rating   |      |         | Unit | Remark            |
|-------------------------|---------|--------|----------|------|---------|------|-------------------|
|                         |         |        | Min      | Typ  | Max     |      |                   |
| Power Voltage           | Logic   | VCC    | 2.5      | 3.3  | 3.6     | V    | Note1             |
| Input Voltage           | L level | VIL    | -0.3     | ---- | 0.2*VCC | V    | VCC=2.5<br>~ 3.6V |
|                         | H level | VIH    | 0.8* VCC | ---  | ---     | V    |                   |
| LCD Drive Power current |         | ILCD   | ---      | ---  | 15      | mA   | ---               |

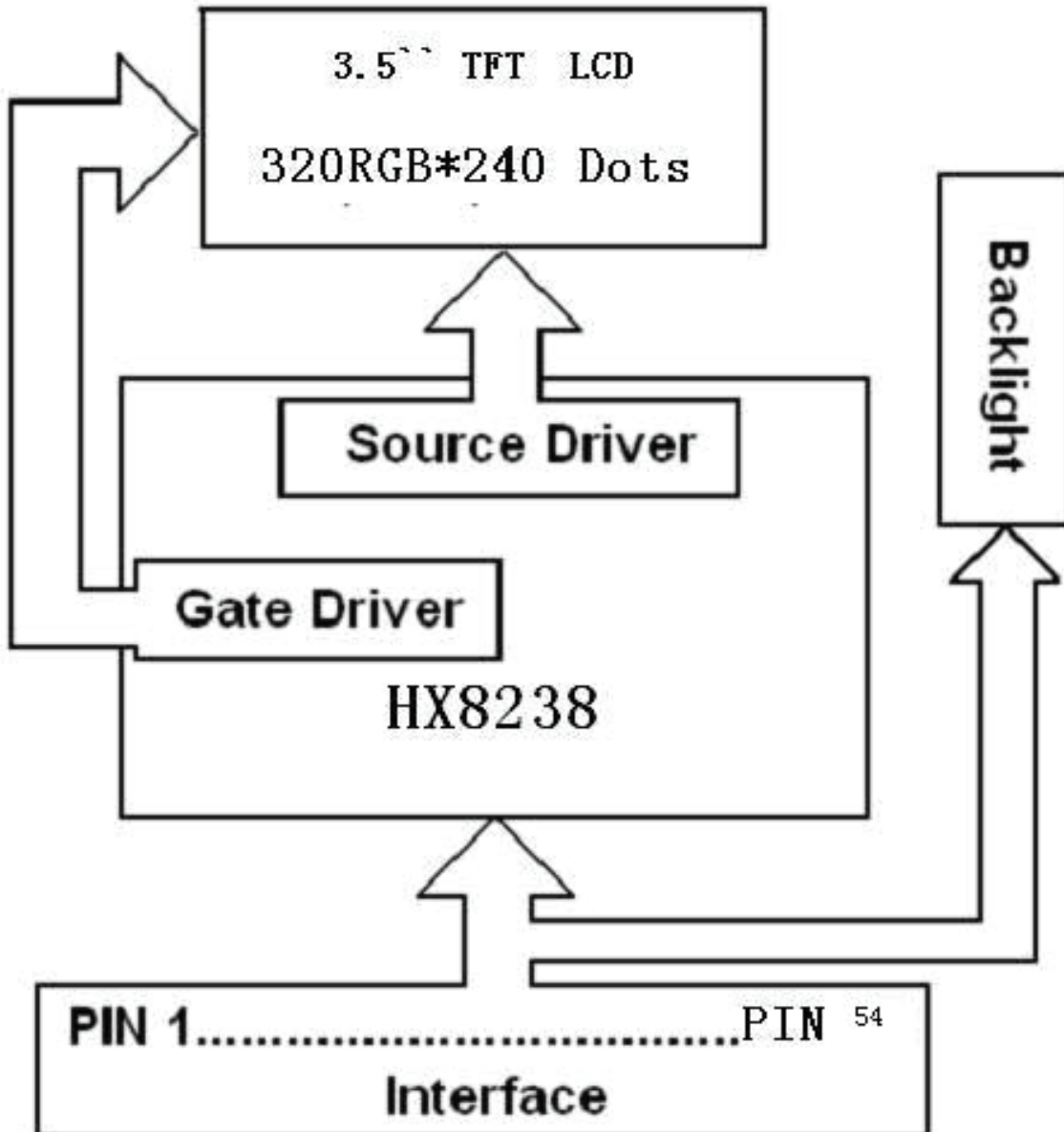
Remark:

Note1:Vcom must be adjusted to optimize display quality: Cross-talk, Contrast Ratio and etc.

|                              |               |                         |                 |                     |               |
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## 7. Module Function Description

### 7-1. Block Diagram Of LCM



|                              |               |                         |                 |                     |               |
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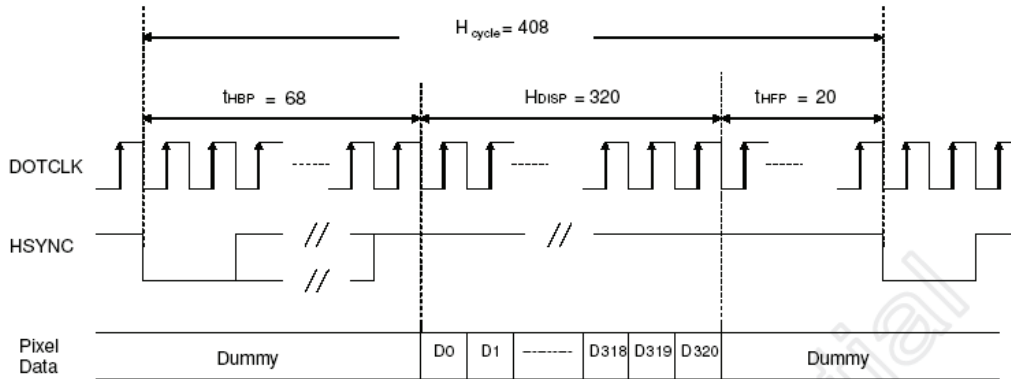
## 7-2. Pin Description

| NO.   | Symbol    | Function                           |
|-------|-----------|------------------------------------|
| 1-2   | LED-K     | CATHODE OF BACKLIGHT LED           |
| 3-4   | LED-A     | ANODE OF BACKLIGHT LED             |
| 5     | NC        | DUMMY PIN                          |
| 6     | /RESET    | SYSTEM Reset                       |
| 7     | NC        | DUMMY PIN                          |
| 8     | NC        | NC                                 |
| 9     | NC        |                                    |
| 10    | NC        |                                    |
| 11    | NC        |                                    |
| 12-35 | DB00-DB23 | PARALLE RGB Data BUS               |
| 36    | HSYNC     | Line synchronization signal        |
| 37    | VSYNC     | Frame synchronization signal       |
| 38    | DCLK      | DOTCLK                             |
| 39    | NC        | DUMMY PINS                         |
| 40    | NC        | DUMMY PINS                         |
| 41    | VCC       | SYSTEM VCC                         |
| 42    | VCC       | SYSTEM VCC                         |
| 43    | SPENA     | SPI ENABLE                         |
| 44-47 | NC        | DUMMY PINS                         |
| 48    | GND       | SYSTEM GND                         |
| 49    | SPCLK     | SERIAL CLOCK SIGNAL                |
| 50    | SPDAT     | SERIAL DATA SIGNAL                 |
| 51    | NC        | DUMMY PINS                         |
| 52    | DEN       | Display enable pin from controller |
| 53    | GND       | SYSTEM GND                         |
| 54    | GND       | SYSTEM GND                         |

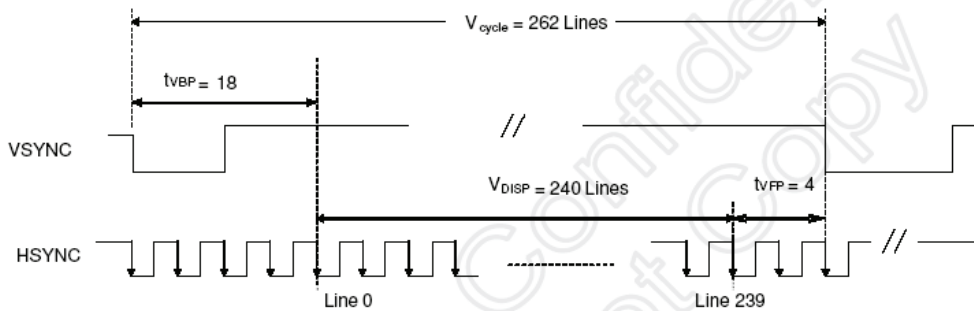
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## 7-3. Timing Characteristics

### 7.3.1 RGB Interface Timing Characteristics



a) Horizontal Data Transaction Timing



VCC=2.5~3.3V , Ta=25°C

| Signal | Symbol             | Parameter                   | Min. | Max. | Unit | description          |
|--------|--------------------|-----------------------------|------|------|------|----------------------|
| RS     | t <sub>AST</sub>   | Address setup time          | 10   | -    | ns   | -                    |
|        | t <sub>AHT</sub>   | Address hold time           | 10   | -    |      |                      |
| /CS    | t <sub>CHW</sub>   | Chip select 'H' pulse width | 0    | -    | ns   | -                    |
|        | t <sub>TCS</sub>   | Chip select setup time      | 35   | -    |      |                      |
|        | t <sub>RCSFM</sub> | Chip select setup time      | 355  | -    |      |                      |
|        | t <sub>CSF</sub>   | Chip select wait time       | 10   | -    |      |                      |
|        | t <sub>CSH</sub>   | Chip select hold time       | 10   | -    |      |                      |
| /WR    | t <sub>WC</sub>    | Write cycle                 | 100  | -    | ns   | -                    |
|        | t <sub>WRH</sub>   | Control pulse 'H' duration  | 35   | -    |      |                      |
|        | t <sub>WRL</sub>   | Control pulse /L/ duration  | 35   | -    |      |                      |
| /RD    | t <sub>RCFM</sub>  | Read cycle                  | 450  | -    | ns   | When read from GRAM. |
|        | t <sub>RDHFM</sub> | Control pulse 'H' duration  | 90   | -    |      |                      |
|        | t <sub>RDLFM</sub> | Control pulse 'L' duration  | 355  | -    |      |                      |
| DB0~15 | t <sub>DST</sub>   | Data setup time             | 15   | -    | ns   | -                    |
|        | t <sub>DHT</sub>   | Data hold time              | 10   | -    |      |                      |
|        | t <sub>RATFM</sub> | Read access time            | -    | 340  |      |                      |
|        | t <sub>ODH</sub>   | Output disable time         | 20   | 80   |      |                      |



|                       |        |                  |          |              |        |
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## 8. Backlight Characteristics

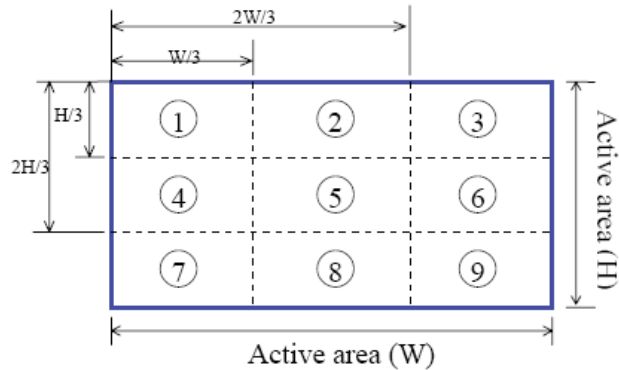
| Item            | Symbol   | Min  | Typ  | Max  | Unit              | Remark |
|-----------------|----------|------|------|------|-------------------|--------|
| Forward voltage | $V_{BL}$ | 18   | 19.2 | 20   | V                 | -      |
| Current         | $I_{BL}$ | -    | 15   | -    | mA/chip           | -      |
| ICE             | X        | 0.26 | -    | 0.32 | -                 | -      |
|                 | Y        | 0.26 | -    | 0.32 | -                 |        |
| Brightness      | -        | 2600 | -    | -    | cd/m <sup>2</sup> | ★1     |
| Uniformity      | -        | 80   | -    | -    | %                 | ★2     |

★1 Test condition is :

- (a) Center point on active area
- (b) Best Contrast

★2 Uniform measure condition :

- (1) Measure 9 point. Measure location is show below :
- (2) Uniform = (Min. brightness / Max. brightness) × 100%
- (3) Best Contrast.

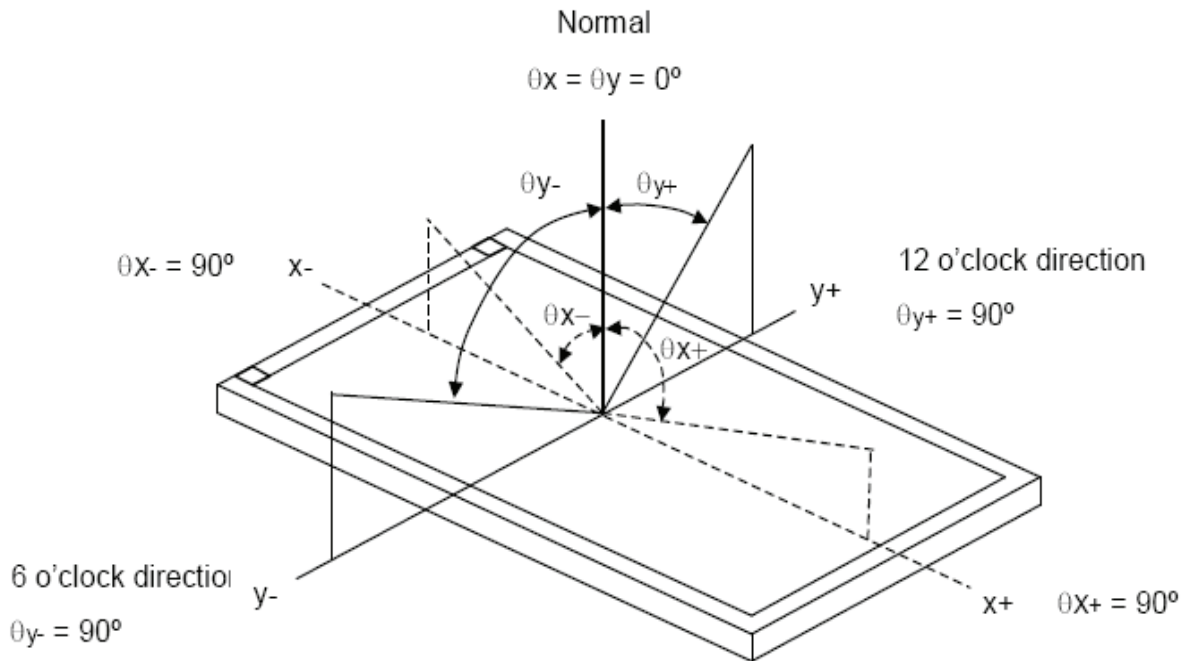


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## 9. Electro-Optical Characteristics

| Item               |       | Symbol        | Condition                 | Min.  | Typ.  | Max.  | Unit | Remark         |
|--------------------|-------|---------------|---------------------------|-------|-------|-------|------|----------------|
| Response time      |       | Tr +Tf        |                           | ---   | 50    | 70    | ms   | Reference Only |
| Contrast Ratio     |       | CR            |                           | 150   | 250   | ---   | ---  |                |
| Transmittance      |       | T%            |                           | ---   | 6.3   | ---   | %    |                |
| Color chromaticity | white | Wx            | $\theta_x = \theta_y = 0$ | 0.264 | 0.294 | 0.324 | -    |                |
|                    |       | Wy            |                           | 0.304 | 0.334 | 0.364 |      |                |
|                    | Red   | Rx            |                           | 0.603 | 0.633 | 0.663 |      |                |
|                    |       | Ry            |                           | 0.296 | 0.326 | 0.356 |      |                |
|                    | Green | Gx            |                           | 0.267 | 0.297 | 0.327 |      |                |
|                    |       | Gy            |                           | 0.547 | 0.577 | 0.607 |      |                |
|                    | Blue  | Bx            |                           | 0.103 | 0.133 | 0.163 |      |                |
|                    |       | By            |                           | 0.099 | 0.129 | 0.159 |      |                |
| Viewing angle      | Hor.  | $\theta_{x+}$ | CR $\geq$ 10              | ---   | 45    | ---   | Deg. |                |
|                    |       | $\theta_{x-}$ |                           | ---   | 45    | ---   |      |                |
|                    | Ver.  | $\theta_{y+}$ |                           | ---   | 35    | ---   |      |                |
|                    |       | $\theta_{y-}$ |                           | ---   | 15    | ---   |      |                |

Note (1) Definition of Viewing Angle  $\theta_x$  and  $\theta_y$ :

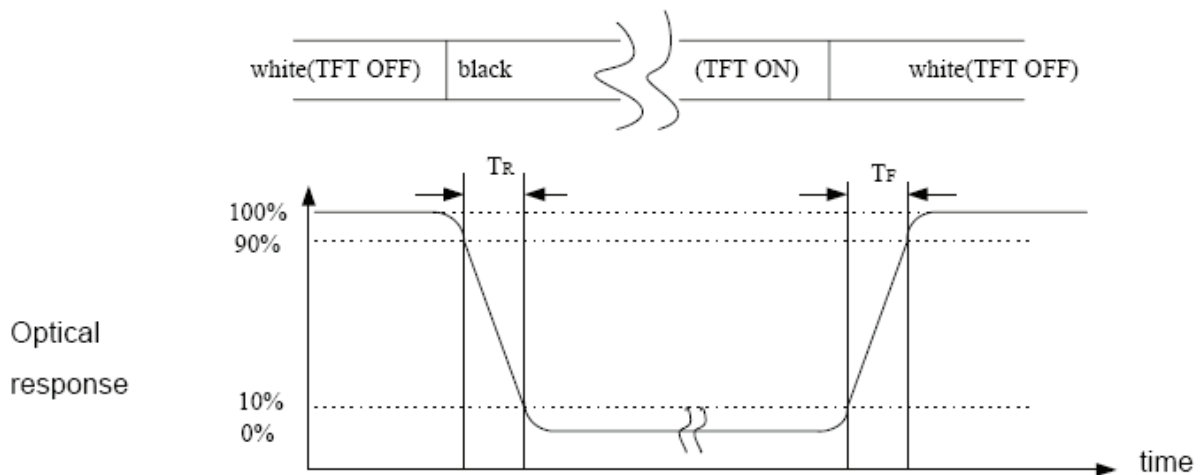


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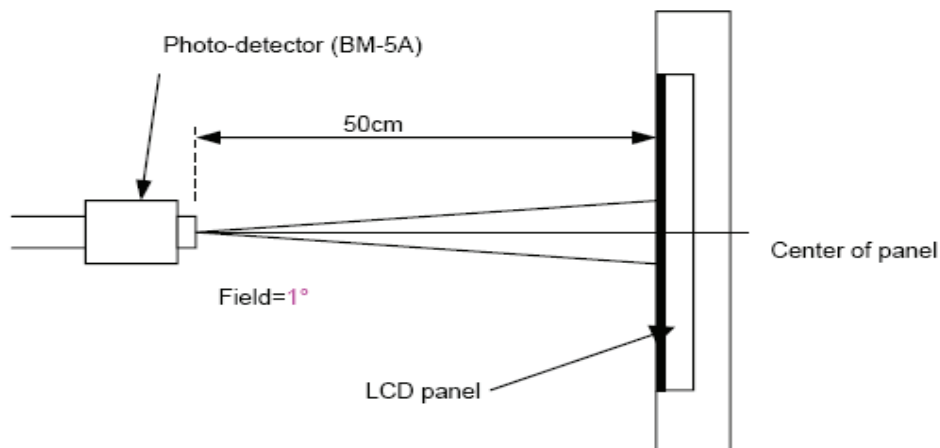
**Note (2) Definition of Contrast Ratio(CR) :**  
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

**Note (3) Definition of Response Time :** Sum of  $T_R$  and  $T_F$



**Note (4) Definition of optical measurement setup**



|                              |               |                         |                 |                     |                |
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## 10. Reliability

### 10.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

### 10.2. Test condition

| NO. | ITEM   | CONDITION                               | CRITERION   |
|-----|--|---|---|
| 1   | High Temperature Non-Operating Test          | 80°C * 240Hrs                           | <ul style="list-style-type: none"> <li>◦ No Defect Of Operational Function In Room Temperature Are Allowable.</li> <li>◦ IDD of LCM in Pre-and Post-Test Should Follow Specification</li> </ul> |
| 2   | Low Temperature Non-Operating Test           | -30°C * 240Hrs                          |   |
| 3   | High Temperature/Humidity Non-Operating Test | 50°C * 90%RH * 240 Hrs                  |   |
| 4   | High Temperature Operating Test              | 70°C * 240Hrs                           |   |
| 5   | Low Temperature Operating Test               | -20°C * 240Hrs                          |   |
| 6   | Thermal Shock Test                           | -30°C (30Min) ↔ 80(30Min)*<br>10 Cycles |   |

#### Notes:

1. Judgments should be made after exposure in room temperature for two hours.
2. The distill water is used for the high temperature / humidity test.
3. The sample above is individually for every reliability tests condition.

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# 11. Inspection Standards

1. AQL(Acceptable Quality Level)  
AQL of major and minor defect

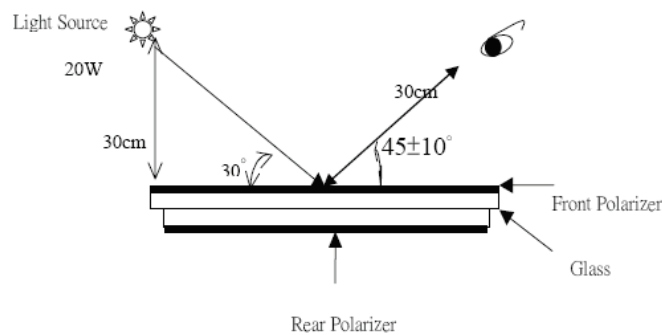
|                  | MAJOR DEFECT | MINOR DEFECT | MAJOR+MINOR |
|------------------|--------------|--------------|-------------|
| APPEARANCE       | 0.40%        | 1.0%         | 1.0%        |
| ELECTRIC-OPTICAL | 0.15%        | 0.15%        | 0.15%       |

## 2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is  $1000\pm 200$ . (Darkroom's lux:  $100\pm 50$ ),

About an angle of incidence 30, a distance of 30cm with normal eye, with an angle of 45 degree to check the products without uncovering the film!

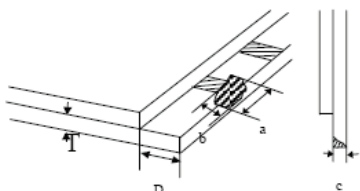
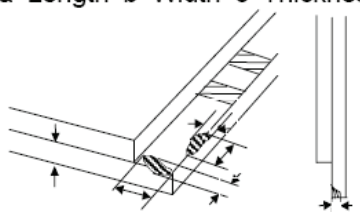
(As shown below)



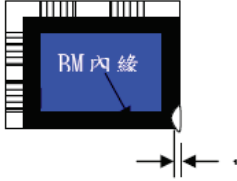
3. Inspection item and criteria
  - 3.1 Visual inspection criterion in immobility
    - 3.1.1 Glass defect

| No | Defect item                                  | Criteria               | Remark |
|----|--|------------------------|--------|
| 1  | Dimension Unconformity<br><br>(Major defect) | By Engineering Drawing |        |

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| No | Defect item  | Criteria   | Remark  |
|----|--|--|---|
| 2  | Cracks<br><br>(Major defect)                                 | 1.Linear cracks on panel<br><b>【Reject】</b><br>2. Nonlinear crack contrast by limited sample   |   |
| 3  | Glass extrude the conductive area<br><br>(minor defect)      | a: disregards and no influence assemblage<br>1) $b \leq 1/3$ Pin width(non bonding area)<br><b>【Accept】</b><br>2) bonding area $\leq 0.5$ mm<br><b>【Accept】</b>  | a:Length, b:Width   |
| 4  | Pin-side · conductive area damaged<br><br>(minor defect)     | (a c : disregards)<br>$b \leq 1/3$ of effective length for bonding electrode<br><b>【Accept】</b>  | a : Length · b : Width · c : Thickness<br>  |
| 5  | Pin-side · non-conductive area damaged<br><br>(minor defect) | 1) Damage area don't touch the ITO (Including contraposition mark,except scribing mark )<br><b>【Accept】</b><br>2) $c < T$ $b \leq BM$ 1/3 of width<br><b>【Accept】</b><br>3) $c = T$<br>b not touch the seal glue<br><b>【Accept】</b><br>4) a disregards | a : Length · b : Width · c : Thickness<br> |

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| No | Defect item                               | Criteria  | Remark   |
|----|---|---|--|
| 6  | Non-pin-side damage<br><br>(minor defect) | c<T<br><br>1) b exceeds 1/3 BM<br><br><b>【Reject】</b>   | c : Thickness    b: width of damage<br><br> |
|    |   | c=T<br>b not touch the seal glue<br><br><b>【Reject】</b> |  |

### 3.1.2 LCD appearance defect (View area)

| No | Defect item  | Criteria                          | Remark    |
|----|--|-----------------------------------|-----------|
| 1  | Fiber · glass<br>crack · polarizer<br>scratch/folded<br><br>(minor defect) | Specification                     | Allowable |
|    |  | 0.05mm<W ≤ 0.1mm;<br>L ≤ 3.0mm    | 1         |
|    |  | W>0.1mm ; L>3.0mm                 | 0         |
| 2  | Polarizer bubble ·<br>concave and convex<br><br>(minor defect)             | $\psi \leq 0.2\text{mm}$          | disregard |
|    |  | 0.2mm< $\psi \leq 0.3\text{mm}$   | 2         |
|    |  | 0.3mm< $\psi \leq 0.5\text{mm}$   | 1         |
|    |  | 0.5mm< $\psi$                     | 0         |
| 3  | Black dots · dirty dots ·<br>impurities · eyewinker<br><br>(Major defect)  | $\psi \leq 0.15\text{mm}$         | disregard |
|    |  | 0.15mm< $\psi \leq 0.25\text{mm}$ | 2         |
|    |  | 0.25mm< $\psi \leq 0.3\text{mm}$  | 1         |
|    |  | 0.3mm< $\psi$                     | 0         |
| 4  | Polarizer prick<br><br>(Major defect)                                      | $\psi \leq 0.1\text{mm}$          | disregard |
|    |  | 0.1mm< $\psi \leq 0.25\text{mm}$  | 3         |
|    |  | $\psi > 0.25\text{mm}$            | 0         |

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### 3.1.3 .FPC

| No | Defect item  | Criteria                  |                   | Remark                                      |
|----|--|---------------------------|-------------------|---|
| 1  | Copper screen peel<br>(Major defect)                                       | Copper screen peel        | <b>【 Reject 】</b> |   |
| 2  | No release tape or peel<br>(Major defect)                                  | No release tape or peel   | <b>【 Reject 】</b> |   |
| 3  | Dirty dot and impurity of<br>FPC for customer using<br>side (minor defect) | Specification             | Allowable         | note1: Cannot have stride ITO<br>impurities |
|    |  | $\psi \leq 0.25\text{mm}$ | 2                 |   |
|    |  | $\psi > 0.25$             | 0                 |   |

### 3.1.4 Black tape & Mara tape

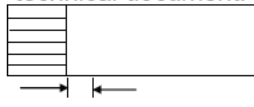
|   |  |   |  |
|---|--|---|--|
| 1 | FPC or H/S black tape<br>shift<br><br>(minor defect) | 1.shift spec:<br>1)glue to the polarize<br><b>【 Reject 】</b><br>2) IC bare<br><b>【 Reject 】</b><br>2. left-and-right spec:<br>1) exceed of FPC edge or H-S<br>edge<br><b>【 Reject 】</b><br>2)IC bare<br><b>【 Reject 】</b> |  |
| 2 | No black tape<br>(Major defect)                      | No black tape<br><b>【 Reject 】</b>  |  |
| 3 | Tape position mistake<br>(minor defect)              | Not by engineering drawing<br><b>【 Reject 】</b>   |  |
| 4 | Mara tape defect<br><br>(minor defect)               | Peel before pulling the protecting<br>film.<br><b>【 Reject 】</b>  |  |

### 3.1.5 Silicon and Tuffy glue

| No | Defect item                           | Criteria   | Remark                                    |
|----|---------------------------------------|--|---|
| 1  | Quantity of silicon<br>(minor defect) | Uncover the ITO and circuit area.<br><b>【 Reject 】</b> | note: compared by engineering<br>drawing. |



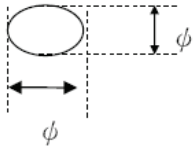
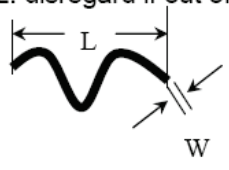
|                       |        |                  |          |              |         |
|-----------------------|--------|------------------|----------|--------------|---------|
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| No | Defect item                              | Criteria  | Remark   |
|----|--|---|--|
| 2  | Tuffy glue<br>(minor defect)             | 1. Uncover the reveal copper area <b>【 Reject】</b><br>2. Cover layer 0.3mm(Min) ~ 3.0mm(Max) <b>【 accept】</b> | note:if customer has special requirement , refer to the technical document.<br> |
| 3  | Depth of glue covering<br>(minor defect) | Depth of glue covering ovetop front Polarizer <b>【 Reject】</b>  | Except of the special requirement °  |

### 3.2 Electrical criteria

| No | Defect item                                    | Criteria                                      | Remark            |
|----|--|---|-------------------|
| 1  | No display<br>(Major defect)                   | No display <b>【 Reject】</b>                   |                   |
| 2  | Missing line<br>(Major defect)                 | Missing line <b>【 Reject】</b>                 |                   |
| 3  | Seg-com light and dark<br>(Major defect)       | Seg-com light and dark <b>【 Reject】</b>       | ND filter 2% test |
| 4  | No display in immobility<br>(Major defect)     | No display in immobility <b>【 Reject】</b>     |                   |
| 5  | Flicker of Pattern<br>(Major defect)           | Flicker of Pattern <b>【 Reject】</b>           |                   |
| 6  | Mura<br>(Major defect)                         | ND filter 2% test                             |                   |
| 7  | Over current<br>(Major defect)                 | Over current <b>【 Reject】</b>                 |                   |
| 8  | Voltage out of specification<br>(Major defect) | Voltage out of specification <b>【 Reject】</b> |                   |
| 9  | Pattern blur ,error code<br>(Major defect)     | Pattern blur ,error code <b>【 Reject】</b>     |                   |
| 10 | Dark light, Flicker<br>(Major defect)          | Dark light, Flicker <b>【 Reject】</b>          |                   |

|                       |        |                  |          |              |         |
|-----------------------|--------|------------------|----------|--------------|---------|
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| No | Defect item   | Criteria  | Allowable | Remark  |
|----|---|---|-----------|---|
| 11 | Black/White dots<br>· Dirty dots · eyewinker<br><br>(Major defect)          | Specification   | Allowable | Note1: disregard if out of AA<br>                                  |
|    |   | $\psi \leq 0.15\text{mm}$   | disregard |   |
|    |   | $0.15\text{mm} < \psi \leq 0.25\text{mm}$                         | 2         |   |
|    |   | $0.25\text{mm} < \psi \leq 0.3\text{mm}$                          | 1         |   |
|    |   | $0.3\text{mm} < \psi$   | 0         |   |
| 12 | Fiber · glass cratch ·<br>polarizer<br>scratch/folded<br><br>(minor defect) | $W \leq 0.03\text{mm}$  | disregard | note1: L : Length · W : Width<br>note2: disregard if out of AA<br> |
|    |   | $0.03\text{mm} < W \leq 0.05\text{mm}$ ;<br>$L \leq 3.0\text{mm}$ | 2         |   |
|    |   | $0.05\text{mm} < W \leq 0.1\text{mm}$ ;<br>$L \leq 3.0\text{mm}$  | 1         |   |
|    |   | $W > 0.1\text{mm}$ ; $L > 3.0\text{mm}$                           | 0         |   |

|                              |               |                         |                 |                     |                |
|------------------------------|---------------|-------------------------|-----------------|---------------------|----------------|
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|                              |               |                         | <i>B</i>        | <i>2012/03/05</i>   | <i>19 / 20</i> |

## 12. Precautions For Using LCD Modules

Please pay attentions to the followings as using the LCD module.

### 12.1 Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the ITO film very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Wipe off water droplets or oil immediately.
- (f) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (g) Do not touch the output pins directly with bare hands.
- (h) Do not disassemble the LCD module.

### 12.2 Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

### 12.3 Operation

- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.

